

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AF/2822

Applicant: Qing Ma

Title: METHOD OF FABRICATING AN INTEGRATED CIRCUIT THAT SEALS A MEMS
DEVICE WITHIN A CAVITY

Docket No.: 884.591US1

Filed: November 2, 2001

Examiner: Jamie L. Brophy

Customer No.: 21186



Serial No.: 10/005667

Due Date: January 27, 2004

Group Art Unit: 2822

Confirmation No.: 7003

MS AF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

☒ A return postcard.

☒ An Amendment and Response Under 37 C.F.R. 1.116 (6 Pages).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Ann M. McCrackin
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of December 2003.

~~Ann M. Richards~~

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Kacia Lee
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EXPEDITED PROCEDURE – EXAMINING GROUP 2822

S/N 10/005667

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Qing Ma	Examiner:	Jamie L. Brophy
Serial No.:	10/005667	Group Art Unit:	2822
Filed:	November 2, 2001	Docket No.:	884.591US1
Title:	METHOD OF FABRICATING AN INTEGRATED CIRCUIT HAVING A MEMS DEVICE		
Assignee:	Intel Corporation		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

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Applicant has reviewed the Final Office Action mailed on October 27, 2003. Please amend the above-identified patent application as follows.

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